

9A4 2824

<b>TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT</b> (Under 37 CFR 1.97(b) or 1.97(c))			Docket No. YOR920010499US1 (14710)	
Re Application Of: Stephen W. Bedell, et al.				
Serial No. 10/055,138	Filing Date 1/23/2002	Examiner Pho M. Luu	Group Art Unit 2824	
Title: METHOD OF CREATING HIGH-QUALITY RELAXED SiGe-ON-INSULATOR FOR STRAINED Si CMOS APPLICATIONS				
Address to: Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450				
37 CFR 1.97(b)				
1. <input checked="" type="checkbox"/> The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office Action on the merits, or before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.				
37 CFR 1.97(c)				
2. <input type="checkbox"/> The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(b), provided that the Information Disclosure Statement is filed before the mailing date of a Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an Action that otherwise closes prosecution in the application, and is accompanied by one of:				
<input type="checkbox"/> the statement specified in 37 CFR 1.97(e);				
OR				
<input type="checkbox"/> the fee set forth in 37 CFR 1.17(p).				

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**TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT**  
(Under 37 CFR 1.97(b) or 1.97(c))

Docket No.  
20020010499 (14710)

In Re Application: Stephen W. Bedell, et al.

Serial No.  
10/055,138

Filing Date  
1/23/2002

Examiner  
Pho M. Luu

Group Art Unit  
2824

**METHOD OF CREATING HIGH-QUALITY RELAXED SiGe-ON-INSULATOR FOR STRAINED  
Si CMOS APPLICATIONS**

**Payment of Fee**

(Only complete if Applicant elects to pay the fee set forth in 37 CFR 1.17(p))

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Leslie S. Szivos, Ph.D.

Typed or Printed Name of Person Mailing Certificate

Dated: June 30, 2003

Leslie S. Szivos, Ph.D.

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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**Applicant(s):** Stephen W. Bedell, et al.

**Examiner:** Pho M. Luu

**Serial No:** 10/055,138

**Art Unit:** 2824

**Filed:** January 23, 2002

**Docket:** YOR920010499US1 (14710)

**For:** METHOD OF CREATING  
HIGH-QUALITY RELAXED  
SiGe-ON-INSULATOR FOR STRAINED  
Si CMOS APPLICATIONS

**Dated:** June 30, 2003

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

Sir:

In accordance with 37 C.F.R. §1.97 and 1.98, it is requested that the following references, which are also listed on the attached Form PTO-1449, be made of record in the above-identified case.

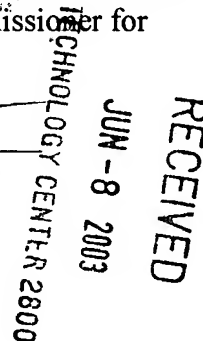
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**CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)**

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Box 1450, Alexandria, VA 22313-1450 on June 30, 2003.

Dated: June 30, 2003

  
Leslie S. Szivos, Ph.D.



The references were cited in a Search Report received from the U.S. Patent Office dated June 4, 2003. Applicants are submitting copies of the cited references, together with a copy of the Search Report. The relevance of the above-identified references has been described in the Search Report.

Inasmuch as this Information Disclosure Statement is being submitted in accordance with the schedule set out in 37 C.F.R. § 1.97(b), no statement or fee is required.

Respectfully submitted,

A handwritten signature in black ink, appearing to read 'LSZ', with a long horizontal flourish extending to the right.

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Two (2) References